said pixel sensors of a logical unit when said selector element is enabled, and converts said analog signal to a converted digital value, and N storage elements, each respectively associated with one of said plurality of analog to digital converters, and each for storing the converted digital value indicating the output signal;

wherein N is at least two.

5.   A method of operating a pixel sensor array, comprising:

obtaining a pixel sensor array of photosensitive elements, each having a photosensitive element in a pixel, a buffer in said pixel associated with said photosensitive element, and a selector transistor in said pixel which is enabled to allow a signal from said pixel to pass, and disabled to block the signal from passing;

connecting a plurality of said outputs of said selector transistors to one another, to form a plurality of logical units, each logical unit formed by a plurality of said output transistors which are connected to one another;

receiving, in a plurality of A/D converter units, a respective plurality of signals from a respective plurality of first logical units, and A/D converting said respective plurality of signals into a respective plurality of converted digital values and storing said respective plurality of converted digital values information in a respective plurality of first storage units;

receiving, in said plurality of A/D converter units, a respective of signals from a respective plurality of second logical units, adjacent to said first logical units, and A/D converting said respective plurality of signals into a respective plurality of converted digital values and storing said respective plurality of converted digital values in a respective plurality of second storage units; and

reading out said information from said A/D conversion unit in a different order than an

2

order in which the information was converted.

7.   A method as in claim 5, wherein said units are linear units which are one of rows and columns, and said different order include a first different order which skips lines between conversions, and a second different order which is a complete order.

Please add new claim 8:

8.   The device of claim 1, wherein each pixel is a CMOS pixel.

3